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Synchronizing pulse generator circuit

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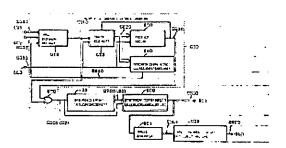
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Abstract of DE4403374

In the circuit, an out-of lock correction section (600) processes a synchronising signal (S601) for providing a corrected synchronising signal (S600) and a synchronising clock generating section (700) which generates a synchronising clock (S700) exactly synchronised with the corrected synchronising signal (S600), and then a synchronising pulse generating section (800) counts the synchronising clock (\$700) for providing a synchronising signal (S800) which is exactly synchronised to the synchronising signal (\$601). The synchronising pulse generating section generates high-accuracy synchronising pulses without parts applied from the outside and being suitable for generating HD pulses for use in a deflection system of a multi-synchronisation picture monitor.



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- 66 Für die Beurteilung der Patentfähigkeit in Betracht gezogene Druckschriften:

38 32 330 A1 GB 20 86 177 A US 47 29 024 JP 63-41 466 B2 JΡ 51-28 188 B2 JΡ 61-11 018 B2

Synchronisierimpulserzeugungsschaltung